



2184 #2  
BT  
10-31-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Gary L. Swoboda

Serial No: 09/924,800

Filed: August 08, 2001

For: Apparatus and Method for Processor Power Measurement  
In A Digital Signal Processor Using Trace Data and Simulation  
Techniques

TI-33319

Art Unit:

Examiner:

RECEIVED  
OCT 19 2001  
Technology Center 2100

LETTER TO THE OFFICIAL DRAFTSPERSON

Ass't Commissioner for Patents

Washington, D.C. 20231

Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)  
I hereby certify that the above correspondence is being deposited  
with the U.S. Postal Service as First Class Mail in an envelope  
addressed to: Ass't Commissioner for Patents, Washington, D.C.  
20231 on September 19, 2001.

*William W. Holloway*  
William W. Holloway, Reg. No. 26,182

Please find enclosed two (2) sheets of formal drawings for the subject case. Charge any necessary fees to Deposit Account No. 20-0668. **This form is submitted in triplicate.**

Respectfully submitted,

*William W. Holloway*

William W. Holloway  
Attorney for Applicant  
Reg. No. 26,182

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